ANALOG WAY LIVECORE^{TM}

Module: MAGIC PACKET

Crestron 3-series

Date: **January 03, 2017**
Driver version: **V3.01**
Compatible with: **LiveCore^{TM} Firmware v4.00.x or above**

GENERAL

This is an optional module for controlling LiveCore^{TM} series processors. It allows you to send magic packet (Wake ON LAN) to a LiveCore^{TM} processor (in sleep mode) identified by its MAC address.

CONNECTION

You have to implement one Crestron UDP/IP Communications object in your project and connect it to the MAGIC PACKET module.

IP address: 255.255.255.255
Default port: **9** (discard)

Control

Main

Wake_PB	Digital_in	Pulse to send a magic packet to the LiveCore^{TM} Processor identified by its MAC address (see parameter below)
To_UDP_Broadcast	String_out	To be connected to UDP/IP Communications TX$

Parameters

address	Param	LiveCore^{TM} processor MAC Address. You can get the Livecore^{TM} device MAC address using the Web RCS: Click the DashBoard button located in the status bar