

ANALOG WAY
 LiveCore™ – Crestron 2-series & 3-series
 MODULE MAIN

CONTACT SUPPORT

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NOTES:	Driver version V3.01, compatible with LiveCore™ Firmware v4.00.x or above
HAND-IN DATE:	January 3, 2017

GENERAL INFORMATION

SIMPLWINDOWS NAME:	LiveCore_Main_V3.01
CATEGORY:	Logic
VERSION:	Driver version V3.01
SUMMARY:	<p>This module is the core module for controlling LiveCore™ series processors. It must be connected to any optional module used in your program. Even if you use only one optional module, the MAIN module (LiveCore_Main) must be included in the project. This module mainly provides inter-modules connectivity and synchronization, global TAKE feature, LiveCore™ processor information feedback as well as some remote control functions.</p> <p>You have to implement one TCP-Client Crestron object in your project and connect it to the MAIN module.</p> <p>IP address: LiveCore™ machine IP address Default port: 10600</p>
GENERAL NOTES:	
CRESTRON HARDWARE REQUIRED:	3-series processor
SETUP OF CRESTRON HARDWARE:	
VENDOR FIRMWARE:	LiveCore™ firmware v4.00.x or above
VENDOR SETUP:	
CABLE DIAGRAM:	TCP-IP

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CONTROL: Main

From_device	S	To be connected to TCP-IP client RX\$
Refresh_General_Statut	D	To be pulsed for initialization after each TCP_IP connection

CONTROL: Inter_connect_screen_modules

You can connect up to 8 optional SCREEN modules (LiveCore_Screen).

X is screen index (1=>8)

ScreenX_Available_FB	D	Screen validity (1 if valid)
From_Module_ScreenX	S	LiveCore_ScreenX module commands to LiveCore™ processor
Mess_Txt_From_Module_ScreenX	S	LiveCore_ScreenX module text status

CONTROL: Inter_connect_screen_preset_modules

You can connect up to 8 PRESET optional modules (LiveCore_Preset).

X is the screen index (1=>8)

From_Module_ScreenX_Presets	S	Module_ScreenX_Presets module commands to LiveCore™ processor
Mess_Txt_From_Module_ScreenX_Presets	S	Module_ScreenX_Presets module status text

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CONTROL: General

Type_RQ_PB	D	Pulse to update the LiveCore™ processor type
Cmd_Set_Ver_RQ_PB	D	Pulse to update the LiveCore™ command set version
Updater_Ver_RQ_PB	D	Pulse to update the LiveCore™ Updater version
Controlers_Count_RQ_PB	D	Pulse to update the number of controllers connected to the LiveCore™ processor
Shutdown_PB	D	Pulse to shutdown LiveCore™ processor (manual restart only)
Sleep_PB	D	Pulse to shutdown LiveCore™ processor and enable Wake on LAN function (see program example to use how to implement wake on LAN in your application)
Reboot_PB	D	Pulse to reboot LiveCore™ processor

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CONTROL: Inter_connect_other_modules

Connections to the optional modules.

From_Module_In	S	Module_In module commands to the LiveCore™ processor
Mess_Txt_From_Module_In	S	Module_In module status text
From_Module_Frame_logo	S	Module_Frame_logo module commands to the LiveCore™ processor
Mess_Txt_From_Module_Frame_logo	S	Module_Frame_logo module status text
From_Module_Presets_Filtering	S	Module_Presets_Filtering module commands to the LiveCore™ processor
Mess_Txt_From_Module_Presets_Filtering	S	Module_Presets_Filtering module status text
From_Module_Master_Presets	S	Module_Master_Presets module commands to the LiveCore™ processor
Mess_Txt_From_Module_Master_Presets	S	Module_Master_Presets module status text
Mess_Txt_From_Module_Snapshot	S	Module_Snapshot module status text
From_Module_Monitoring_Master	S	Module_Monitoring module commands to the LiveCore™ processor (master device)
Mess_Txt_From_Module_Monitoring_Master	S	Module_Monitoring module status text (master device)
From_Module_Monitoring_Slave	S	Module_Monitoring module commands to the LiveCore™ processor (slave device)
Mess_Txt_From_Module_Monitoring_Slave	S	Module_Monitoring module status text (slave device)
From_Module_GPIO	S	Module_GPIO module commands to the LiveCore™ processor
Mess_Txt_From_Module_GPIO	S	Module_GPIO module status text
From_Module_CONFIDENCE	S	Module_CONFIDENCE module commands to the LiveCore™ processor
Mess_Txt_From_Module_CONFIDENCE	S	Module_CONFIDENCE module status text

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FEEDBACK : Main

To_device	S	To be connected to TCP-IP client TX\$
Statut_in_progress_FB	D	Module initialization in progress
Refresh_Next_Module_OS	D	To be connected to next module for daisy chain initialization
Refresh_All_Modules_OS	D	Pulsed to update all the modules

FEEDBACK : Inter_connect_screen_modules

You can connect up to 8 optional SCREEN modules (LiveCore_Screen).

X is screen index (1=>8)

To_Module_ScreenX	S	From TCP-IP client RX\$ to the LiveCore_ScreenX module
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FEEDBACK : Inter_connect_screen_preset_modules

You can connect up to 8 PRESET optional modules (LiveCore_Preset).

X is the screen index (1=>8)

To_Module_ScreenX_Presets	S	From TCP-IP client RX\$ to the Module_ScreenX_Presets module
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FEEDBACK : General

User_Messages_TXT	S	Status message to be displayed in user interface
Device_Type\$	S	LiveCore™ processor type
Cmd_Set_Ver\$	S	LiveCore™ processor version
Updater_Ver\$	S	LiveCore™ processor Updater version
Build_Ver\$	S	LiveCore™ processor TPP version
Controllers_Count_FB	A	Number of controllers connected to the LiveCore™ processor
NB_Screen_Available_FB	A	Number of valid screens. To be sent to LiveCore_Master_Presets module
OutputX_Name_FB	S	Ouptut X label (16 char. Max)
OutputX_HDCP_State_FB	D	HDCP status for output X
Status_Machine_FB	A	LiveCore™ processor global state. See table below for values

FEEDBACK : Inter_connect_other_modules

Connection to the optional modules.

To_Module_In	S	From TCP-IP client RX\$ to the Module_In module
To_Module_Frame_logo	S	From TCP-IP client RX\$ to the Module_Frame_logo module
To_Module_Presets_Filtering	S	From TCP-IP client RX\$ to the Module_Presets_Filtering module
To_Module_Master_Presets	S	From TCP-IP client RX\$ to the Module_Master_Presets module
To_Module_Monitoring_Master	S	From TCP-IP client RX\$ to the module LiveCore_Monitoring (master device)
To_Module_Monitoring_Slave	S	From TCP-IP client RX\$ to the module LiveCore_Monitoring (slave device)
To_Module_GPIO	S	From TCP-IP client RX\$ to the module LiveCore_GPIO
To_Module_CONFIDENCE	S	From TCP-IP client RX\$ to the module LiveCore_CONFIDENCE

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LiveCore™ global states	
0	Unknown
1	Initializing
2	Recalling configuration
3	Linking to secondary LiveCore™ processor
4	Restoring factory settings
5	Updating firmware
255	Ready